Application No.: TBA 3 Docket No.: 8733.494.20-US

## IN THE CLAIMS

1. (Original) An array substrate for use in a liquid crystal display device comprising: a plurality of gate lines on a substrate;

a plurality of data lines over the substrate, each data line being perpendicular to each gate line, wherein a plurality of pixel regions are defined by the data and gate lines;

a common line on the substrate in a respective pixel region, the common line being parallel with and spaced apart from the gate line;

a plurality of common electrodes in the respective pixel region extended from the common line and elongated along a respective data line defining the respective pixel region, wherein each common electrode has a plurality of bend portions, and wherein each common electrode has a substantially sawtooth-shaped base in a region where each common electrode intersects the common line at an obtuse angle with the common line;

a plurality of pixel electrodes in the respective pixel region spaced apart from and elongated along the said common electrodes, wherein each pixel electrode has a plurality of bend portions and corresponds to each common electrode;

a connecting line in the respective pixel region contacting one end of each pixel electrode, the connecting line electrically connecting said pixel electrodes;

a switching element in the respective pixel region electrically connected with a respective gate line defining the respective pixel region and the respective data line, the switching element supplying voltage to the said pixel electrodes.

2-33. (Cancelled)